L Number	Hits	Search Text	DB	Time stamp
1	42177	master same slave	USPAT;	2003/03/29 14:17
			US-PGPUB;	
			EPO; JPO;	
			DERWENT	
2 '	26321	system adj clock	USPAT;	2003/03/29 14:17
			US-PGPUB;	
		· ·	EPO; JPO;	
2	2116		DERWENT	2002/02/20 14:17
3	3116	transmit adj clock	USPAT;	2003/03/29 14:17
			US-PGPUB; EPO; JPO;	
			DERWENT	
4	71	(transmit adj clock) near3 master	USPAT;	2003/03/29 14:17
-	, -	l learning and order, nears master	US-PGPUB;	2003,03,23 11.11
		·	EPO; JPO;	
			DERWENT	
5	35385	phase near2 adjust\$4	USPAT;	2003/03/29 14:35
			US-PGPUB;	
			EPO; JPO;	
			DERWENT	
6	58	1 ,	USPAT;	2003/03/29 14:19
		adjust\$4)	US-PGPUB;	
			EPO; JPO;	
	_		DERWENT	
8	1	1 ,	USPAT;	2003/03/29 14:20
		clock) and ((transmit adj clock) same	US-PGPUB;	
		(phase near2 adjust\$4)))	EPO; JPO;	
7	10	/	DERWENT	2002/02/20 14:25
7	10		USPAT;	2003/03/29 14:25
		clock) same (phase near2 adjust\$4))	US-PGPUB; EPO; JPO;	
		·	DERWENT	
9	419	(master same slave) and (transmit adj	USPAT;	2003/03/29 14:25
١	113	clock)	US-PGPUB;	2003/03/23 14.23
		Clocky	EPO; JPO;	
			DERWENT	
10	15	(master same slave) and ((transmit adj	USPAT;	2003/03/29 14:26
		clock) same (phase near2 adjust\$4))	US-PGPUB;	
		•	EPO; JPO;	
			DERWENT	
11	14	((master same slave) and ((transmit adj	USPAT;	2003/03/29 14:29
		clock) same (phase near2 adjust\$4))) not	US-PGPUB;	9
		((system adj clock) and ((transmit adj	EPO; JPO;	
		clock) same (phase near2 adjust\$4)))	DERWENT	
12	4775	(phase near2 adjust\$4) same clock	USPAT;	2003/03/29 14:31
			US-PGPUB;	
		·	EPO; JPO;	
13	294	((phago poar2 adduct64) come alect)	DERWENT	2002/02/20 14:31
10	294	((phase near2 adjust\$4) same clock) and (master same slave)	USPAT; US-PGPUB;	2003/03/29 14:31
		Immorer same prave)	EPO; JPO;	
			DERWENT	
14	9	(system adj clock) and (transmit adj	USPAT;	2003/03/29 14:31
• =		clock) and (((phase near2 adjust\$4) same	US-PGPUB;	=300,00,20 = 11.01
		clock) and (master same slave))	EPO; JPO;	
			DERWENT	
15	76	phase near2 adjust\$4 near3 arbitrar\$4	USPAT;	2003/03/29 14:36
		<u>-</u>	US-PGPUB;	
			EPO; JPO;	
			DERWENT	
16	0	,	USPAT;	2003/03/29 14:36
		adjust\$4 near3 arbitrar\$4)	US-PGPUB;	
			EPO; JPO;	
1.7			DERWENT	
17	428	(phase near2 adjust\$4) same arbitrar\$4	USPAT;	2003/03/29 14:36
			US-PGPUB;	
			EPO; JPO;	
	l		DERWENT	1

18					
19	18	13	, , , , , , , , , , , , , , , , , , ,	USPAT;	2003/03/29 14:45
19   9   (phase adj shift\$4) near3 (transmit adj clock)   05870277   058702			adjust\$4) same arbitrar\$4)		
19					
	1	_			
20   3289   (phase adj shift\$4) near3 clock	19	9			2003/03/29 15:06
DERMENT   USPAT; USPA	1		clock)		
20   3289   (phase adj shift\$4) near3 clock   USPAT; US-PGUB; EPO; JPO; DERWENT USPAT; US-PGUB; EPO; JPO; DERWENT USPAT; USPAT	1				
232		2200	/	i	2002/02/20 14 50
232	20	3289	(phase ad) shift \$4) hears clock	· ·	2003/03/29 14:50
21   232   (master same slave) and ((phase adj shift\$4) near3 clock)   SPAT; shift\$4) near3 clock)   (Pro; JPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO					
232					
Shift\$4) near3 clock    US-PGPUB; EDG, JPO; DERWENT   US-PGPUB;	21	232	(master same slave) and ((nhase adi		2003/03/29 14.51
22   25	21	232		· ·	2003/03/29 14.31
22   25			SHITC94) HealS Clock)	1	
22   25	1				
Slave) and ((phase adj shift\$4) near3	22	25	(transmit add clock) and ((master same		2003/03/29 15:04
Clock)   Clock)   Clock)   Clock)   Clock)   Clock)   Clock)   Clock)   Clock)   Clock   Clock   Clock)   Clock   Cl	22	23	1 (		2003/03/23 13:04
23   6613   dll (delay adj lock\$4 adj loop)				•	
23			CIOCKII		
24   9297   feedback near3 phase	23	6613	dll (delay adi lock\$4 adi loop)		2003/03/29 15:05
24   9297   feedback near3 phase   RPO, JPO; DERWENT USPAT; US-PGPUB; EPO, JPO; DERW	23	0013	dir (deray ad) rocky4 ad) roop/		2003/03/23 13:03
Seminary   Depart	i				
24   9297   feedback near3 phase   USPAT; US-PGPUB; EPO; JPO; DERWENT USPAT; USPAT; US-PGPUB; EPO; JPO; DERWENT USPAT;					
25	24	9297	feedback near3 phase		2003/03/29 15:05
25			pinate	•	
25		1			
122	1	1			
(feedback near3 phase)  (feedback near3 phase)  (phase adj shift\$4)  (phase adj shift\$4)  (dll (delay adj lock\$4 adj loop)) same (feedback near3 phase)) same (feedback near3 phase) and (febase adj shift\$4) and (feedback near3 phase) and (feedback nea	25	122	(dll (delay adi lock\$4 adi loop)) same		2003/03/29 15:05
26				•	
DERWENT USPAT; US-PGPUB; EPO; JPO; JPO; DERWENT USPAT; US-PGPUB; EPO; JPO; JPO; JPO; DERWENT USPAT; US-PGPUB; EPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	İ		,,		
27   9 ((dl1 (delay adj lock\$4 adj loop)) same (feedback near3 phase)) same ((phase adj shift\$4))   2003/03/29 15:56   270; JPO; DERWENT (SPAT; US-PGPUB; EPO; JPO; DERWENT (SPAT; US-PGPUB;					
27   9	26	112556	(phase adj shift\$4)	USPAT;	2003/03/29 15:24
27				US-PGPUB;	
27				EPO; JPO;	
(feedback near3 phase)) same ((phase adj shift\$4))  28 6658 (phase adj shift\$4) same ("90" adj degrees)  29 15 (dll (delay adj lock\$4 adj loop)) and (feedback near3 phase) and ((phase adj shift\$4) same ("90" adj degrees))  30 310 (dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4))  ((phase adj shift\$4))  31 20 (dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees))  32 5058 (phase adj shift\$4) near4 ("90")  33 0 (dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90")) not ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90")) not ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees))  34 310 ((phase adj shift\$4) same ("90" adj degrees))  35 0 ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees))  36 0 ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  37 0 ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  38 0 0 ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  39 0 0 ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))				DERWENT	
Shift\$4)   EPO; JPO; DERWENT   USPAT; US-PGPUB; US-PGPUB; EPO; JPO; DERWENT   USPAT; US-PGPUB; US-PGPUB; EPO; JPO; DERWENT   USPAT; US-PGPUB; US	27	9	((dll (delay adj lock\$4 adj loop)) same	USPAT;	2003/03/29 15:56
28   6658 (phase adj shift\$4) same ("90" adj degrees)   DERWENT USPAT; USPAT;   2003/03/29 15:53   2003/03/29 15:53   2003/03/29 15:53   2003/03/29 15:53   2003/03/29 15:53   2003/03/29 15:53   2003/03/29 15:53   2003/03/29 15:47   2003/03/29 15:47   2003/03/29 15:47   2003/03/29 15:47   2003/03/29 15:47   2003/03/29 15:47   2003/03/29 15:47   2003/03/29 15:47   2003/03/29 15:48   2003/03/29 15:48   2003/03/29 15:48   2003/03/29 15:48   2003/03/29 15:48   2003/03/29 15:48   2003/03/29 15:48   2003/03/29 15:48   2003/03/29 15:48   2003/03/29 15:48   2003/03/29 15:48   2003/03/29 15:48   2003/03/29 15:55   2003/03/29 15:57   2003/03			(feedback near3 phase)) same ((phase adj	US-PGPUB;	
28 6658 (phase adj shift\$4) same ("90" adj degrees)  29 15 (dll (delay adj lock\$4 adj loop)) and (feedback near3 phase) and ((phase adj shift\$4) same ("90" adj degrees))  30 310 (dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4))  31 20 (dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees))  32 5058 (phase adj shift\$4) near4 ("90")  33 0 (dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90")) not ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90"))) not ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90"))) not ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  34 310 ((phase adj shift\$4) same ("90" adj degrees)))  36 ((phase adj shift\$4) same ("90" adj degrees)))  37 ((phase adj shift\$4) same ("90" adj degrees)))  38 ((phase adj shift\$4) same ("90" adj degrees)))  39 ((phase adj shift\$4) same ("90" adj degrees)))  30 ((phase adj shift\$4) same ("90" adj degrees)))			shift\$4) )	EPO; JPO;	
degrees   degrees   US-PGPUB; EPO; JPO; DERWENT   USPAT; USPAT; USPAT; USPAT; USPAT					
29 15 (dl1 (delay adj lock\$4 adj loop)) and (feedback near3 phase) and ((phase adj shift\$4) same ("90" adj degrees))  30 310 (dl1 (delay adj lock\$4 adj loop)) same ((phase adj shift\$4))  31 20 (dl1 (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees))  32 5058 (phase adj shift\$4) near4 ("90")  33 0 ((dl1 (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90")  31 0 ((dl1 (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90"))  32 1 0 ((dl1 (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90"))  33 1 0 ((dl1 (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  34 310 ((phase adj shift\$4) same ("90" adj degrees)))  35 0 ((dl1 (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  36 0 ((dl1 (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  36 0 ((dl1 (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  37 0 ((dl1 (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  38 0 0 ((dl1 (delay adj lock\$4 adj loop)) same (phase adj shift\$4) same ("90" adj degrees)))	28	6658		•	2003/03/29 15:53
15			degrees)		
15		]			
(feedback near3 phase) and ((phase adj shift\$4) same ("90" adj degrees))  30			· · · · · · · · · · · · · · · · · · ·		
Shift\$4) same ("90" adj degrees))	29	15		· ·	2003/03/29 15:47
30 310 (dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4))  31 20 (dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees))  32 5058 (phase adj shift\$4) near4 ("90")  33 0 ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90")  34 310 ((phase adj shift\$4) same ("90" adj degrees)))  36 310 ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90"))) not ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  34 310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  36 310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  37 310 (phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))			(Teedback near3 phase) and ((phase adj		
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31 20 (dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees))  32 5058 (phase adj shift\$4) near4 ("90")  33 0 ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90")) not ((phase adj shift\$4) near4 ("90"))) not ((phase adj shift\$4) near4 ("90"))) not ((phase adj shift\$4) same ("90")) same ((phase adj shift\$4) same ("90" adj degrees)))  34 310 ((phase adj shift\$4)) same (dll (delay degrees)))  36 310 ((phase adj shift\$4)) same (dll (delay degrees)))  37 310 ((phase adj shift\$4)) same (dll (delay degrees)))  38 310 ((phase adj shift\$4)) same (dll (delay degrees)))  39 310 ((phase adj shift\$4)) same (dll (delay degrees))) ((phase adj shift\$4)) same (dll (delay degrees)) ((phase adj shift\$4)) ((phase adj shift\$4)) ((phase adj shift\$4)) same (dll (delay degrees)) ((phase adj shift\$4)) ((			((pnase adj sniit\$4))		
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32					
32			acgrees,,		
US-PGPUB; EPO; JPO; DERWENT USPAT; ((phase adj shift\$4) near4 ("90"))) not ((dll (delay adj lock\$4 adj loop)) same ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees))) ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  310  US-PGPUB; EPO; JPO; USPAT; US-PGPUB; EPO; JPO;	32	5052	(phase adi shift\$4) pear4 ("90")		2003/03/20 15.55
33 0 ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90"))) not ((phase adj shift\$4) same ("90" adj ((phase adj shift\$4) same ("90" adj ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  34 310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  34 310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  36 310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  37 310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))			(phase aa) shirteyi/ heari ( 30 )	· · · · · · · · · · · · · · · · · · ·	2003/03/23 13:33
O ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90"))) not ((phase adj shift\$4) same ("90" adj ((phase adj shift\$4) same ("90" adj ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  32003/03/29 15:55  3310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  332 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))					
33 0 ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90"))) not ((dll (delay adj lock\$4 adj loop)) same ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  34 310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  34 310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  36 310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  37 310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))			,		
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((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))  310 ((phase adj shift\$4)) same (dll (delay adj lock\$4 adj loop))  32003/03/29 15:57 US-PGPUB; EPO; JPO;				·	2000/00/27 10:00
((phase adj shift\$4) same ("90" adj degrees)))  310 ((phase adj shift\$4) ) same (dll (delay uspat; adj lock\$4 adj loop))  USPAT; US-PGPUB; EPO; JPO;	1			· ·	
degrees))) 310 ((phase adj shift\$4) ) same (dll (delay uspat; adj lock\$4 adj loop)) US-PGPUB; EPO; JPO;					
310 ((phase adj shift\$4) ) same (dll (delay USPAT; adj lock\$4 adj loop))					
adj lock\$4 adj loop)) US-PGPUB; EPO; JPO;	34	310		USPAT;	2003/03/29 15:57
EPO; JPO;					
			- <del>-</del>		

Search History

3/29/03 4:25:02 PM

Page 2

35	113	driver and (((phase adj shift\$4) ) same	USPAT;	2003/03/29 15:58
		(dll (delay adj lock\$4 adj loop)))	US-PGPUB;	
			EPO; JPO;	
			DERWENT	
36	309937	feedback	USPAT;	2003/03/29 15:59
			US-PGPUB;	
			EPO; JPO;	
			DERWENT	
37	73	(driver and (((phase adj shift\$4) ) same	USPAT;	2003/03/29 15:59
		(dll (delay adj lock\$4 adj loop)))) and	US-PGPUB;	
		feedback	EPO; JPO;	
			DERWENT	
38	64	[ , ( = = = , , , , = = = , , , , = = = , , , , = = = , , , , = = = , , , , = = = , , , , = = , , , , = = , , , , = = , , , , = = , , , , = = , , , , = , , , = , , , = , , , = , , , = , , , = , , , = , , , = , , , = , , , = , , , = , , , = , , , = , , = , , , = , , = , , = , , = , , = , , = , , = , , = , , = , , = , , = , , = , , = , , = , = , , =	USPAT;	2003/03/29 15:59
		(dll (delay adj lock\$4 adj loop)))) and	US-PGPUB;	
		feedback) not ((dll (delay adj lock\$4 adj	EPO; JPO;	
		loop)) same ((phase adj shift\$4) same	DERWENT	1
1		("90" adj degrees)))		1